

Day : Friday
Date: 5/26/2006

Time: 18:19:01

 **PALM INTRANET**

Inventor Information for 10/000036

Inventor Name	City	State/Country
NAVEN, FINBAR	CHEADLE HUME	UNITED KINGDOM
SOU, ANTONY	WHITEFIELD	UNITED KINGDOM
RASHMAN, WAYNE ERIC	PRESTWICH	UNITED KINGDOM

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Inventor Name Search Result

Your Search was:

Last Name = RASHMAN

First Name = WAYNE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10000036	Not Issued	71	12/04/2001	Processing high-speed digital signals	RASHMAN, WAYNE ERIC

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	<input type="button" value="Search"/>
	<input type="text" value="RASHMAN"/>	<input type="text" value="WAYNE"/>	

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 **PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = SOU

First Name = ANTONY

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10000036	Not Issued	71	12/04/2001	Processing high-speed digital signals	SOU, ANTONY
10315088	Not Issued	41	12/10/2002	Clock recovery circuitry	SOU, ANTONY
11138325	Not Issued	30	05/27/2005	Method of sampling data and a circuit for sampling data	SOU, ANTONY
60574673	Not Issued	159	05/27/2004	Method of sampling data and a circuit for sampling data	SOU, ANTONY

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
<input type="text" value="SOU"/>	<input type="text" value="ANTONY"/>	<input type="button" value="Search"/>

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Inventor Name Search Result

Your Search was:

Last Name = NAVEN

First Name = FINBAR

Application#	Patent#	Status	Date Filed	Title	Inventor Name
06770745	4697268	150	08/29/1985	DATA PROCESSING APPARATUS WITH MESSAGE ACCEPTANCE MONITORING	NAVEN, FINBAR
06840602	4701916	150	03/17/1986	DIGITAL INTEGRATED CIRCUIT WITH MULTI-MADE REGISTER	NAVEN, FINBAR
06882947	4730317	150	07/07/1986	DIGITAL INTEGRATED CIRCUITS	NAVEN, FINBAR
06882949	4730316	150	07/07/1986	DIGITAL INTEGRATED CIRCUITS	NAVEN, FINBAR
08688093	5936956	150	07/29/1996	DATA RECEIVING DEVICES	NAVEN, FINBAR
09192488	6418077	150	11/17/1998	MEMORY ACCESS METHODS AND DEVICES FOR USE WITH RANDOM ACCESS MEMORIES	NAVEN, FINBAR
09304843	6810043	150	05/05/1999	SCHEDULING CIRCUITRY AND METHODS	NAVEN, FINBAR
10000036	Not Issued	71	12/04/2001	Processing high-speed digital signals	NAVEN, FINBAR
10043138	6556506	150	01/14/2002	MEMORY ACCESS METHODS AND DEVICES FOR USE WITH RANDOM ACCESS MEMORIES	NAVEN, FINBAR
10163457	Not Issued	93	06/07/2002	DEVICE AND METHOD FOR ENSURING THAT A SIGNAL ALWAYS REACHES ITS DESTINATION AFTER A FIXED NUMBER OF CLOCK CYCLES	NAVEN, FINBAR
60550058	Not Issued	159	03/05/2004	Method for congestion management of a network, a signalling protocol, a switch, an	NAVEN, FINBAR

				end station and a network	
<u>60622995</u>	Not Issued	159	10/29/2004	Method for congestion management of a network, a signalling protocol, a switch, an end station and a network	NAVEN, FINBAR
<u>60789157</u>	Not Issued	20	04/05/2006	Method for congestion management of a network, a switch, and a network	NAVEN, FINBAR

Inventor Search Completed: No Records to Display.

	Last Name	First Name	
Search Another: Inventor	<input type="text" value="NAVEN"/>	<input type="text" value="FINBAR"/>	<input type="button" value="Search"/>

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